


duplex mode of operation by the processing means. The cited section merely recites that individual encryption devices may be programmed to accept only certain commands. Thus, the device would not be disabled as recited by the applicants claims but merely programmed to selectively process certain types of instructions. Furthermore, this acceptance of only certain commands is based upon individual programming by a user and is not responsive to an indication of whether or not a UART is operating within a full duplex mode of operation. Thus, since the cited reference fails to disclose means for indicating whether the cited input/output port (processor) is operating in a full duplex mode of operation and fails to disclose that the device may be disabled in response to an indication of the full duplex mode of operation, the applicant respectfully submits that the Hamilton reference fails to disclose the applicant's invention, as claimed, and respectfully requests withdrawal of the Examiners objection and a Notice of Allowance for all the claims.

With respect to the Examiner's comments to Claims 16 and 17, Figure 5a does not teach that a receiver enable flag has been set. On the contrary, Figure 5a discloses the setting of a master flag and reset flag not a flag indicating whether or not a receiver in a UART is enabled or disabled. Applicant's Claim 17 recites that a receiver control is disabled in response to an indication of the state of the enable flag. The figures cited by the Examiner in reference to Claim 17 appear to be related to the setting of an

option through an options menu subroutine and enabling/disabling or setting passwords. The cited sections do not disclose enabling or disabling of a receiver.

Therefore, and in view of the Hamilton references failure to disclose the intention as claimed, the Applicant respectfully requests withdrawal of the Examiner's rejections and a Notice of Allowance for all remaining claims.

Respectfully submitted,

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